REMARKS

Applicants have submitted support for the above additional claims in the attached Status of Claims and Support for Claim Changes. In addition, Applicants respectfully submit that no new matter has been added.

As set forth in the Patentee's declaration, proposed new independent claim 11 is a method claim corresponding to operations set forth in allowed independent apparatus claims, such as issued claim 4 and issued claim 10 but does not include reference to a video decoder.

New claim 12 is believed to further narrow new claim 11 by adding previously unclaimed subject matter relating to the storing of the digital video in memory wherein the stored digital video in memory does not include the embedded data access parameter. The additional dependent claims 13 and 14 are believed to further narrow the new claim 11.

New independent claim 15 is a broadening apparatus claim not previously presented which is broadening in at least that it claims at least one of a central processing unit and another computer element. In addition it is believed to be narrower in at least that that it claims memory coupled to the video decoder for storing the digital video when the stored digital video does not include the data access parameter. This combination was not previously presented in its current form for consideration.

New Claim 16 is a method claim directed to another aspect of the invention not believed to be previously presented for examination. This claim is believed to be broadening in at least that it is not directed to the operation of a video decoder and instead may be carried out by a central processing unit, computer element, peripheral device or other suitable structure.

Dependent claims 17 and 18 are believed to further narrow new independent claim 16.

New claim 19 is a new system claim directed to an aspect of the invention not previously claimed which includes, memory containing stored digital video data obtained from an analog video signal wherein the stored digital video data does not include the embedded data access parameter from the analog video signal and at least one of a CPU, computer element and peripheral device that is operative to process stored digital video data that does not include an embedded access parameter from an analog video signal, in accordance with a received indication of data access restriction.

Dependent claims 20 and 21 are believed to further narrow new independent claim 19.

Applicants respectfully request examination of the above claims and respectfully submit that the claims are in condition for allowance. If the Examiner believes that prosecution may be expedited through telephone conference, the Applicants' attorney invites the Examiner to contact him at the below number.

Respectfully submitted,

Date: March 12, 2004

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Edward G. Callway et al.	Examiner: na
Patent No.: 6,356,704	Art Group: na
Issue Date: March 12, 2002	Attorney Docket No. 00100.99.1035

Title: **METHOD AND APPARATUS FOR DETECTING PROTECTION OF AUDIO AND VIDEO SIGNAL**

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3-12-04 Christine A. Wright
Date *Christine A. Wright*

STATUS OF CLAIMS AND SUPPORT FOR CLAIM CHANGES (37 C.F.R. §1.173(c))

The status of the claims as a result of the amended submitted herewith is:

Claims canceled: None

Claims amended: None

Claims added: 11-21

The new claims have been reproduced below with support in the patent indicated in brackets. It is respectfully noted that the column and line support set forth below include the corresponding figures and are merely examples of support and additional support may be found throughout the patent.

As to claim 11, this new claim is a new category claim (method versus apparatus) to a process of operations set forth in, for example, claims 4 and 10 of the issued patent. The claims as set forth below indicate at least one example of support for the claims in the patent.

11. (New) A method for a computing system to provide protection of incoming data, the method comprising:

receiving data of at least one of analog audio data and analog video data, wherein a line of the data includes screen end information, a data access parameter, color burst information, and at least one of audio and video data and wherein the data access parameter is independent of a source of the data [at least col. 2, line 18 – col. 3, line 57];

digitizing, independent of the data access parameter, at least one of audio and video data to produce digital video, wherein once the at least one of audio and video data is digitized, the data access parameter is lost [at least col. 2, lines 49-60];

prior to enabling a central processing unit to access the digital video, determine whether the data access parameter restricts accessing of the digital video [col. 2, line 18 – col. 3, line 29]; and

when the data access parameter restricts accessing of the digital video, prevent the central processing unit from accessing the digital video without restriction [col. 2, line 18 – col. 3, line 29; col. 4, line 1 – col. 5, line 27].

12. (New) The method of claim 11 wherein the incoming data includes an embedded data access parameter and wherein the method includes storing the digital video in memory wherein the stored digital video in the memory does not include the embedded data access parameter [at least col. 2, lines 55-60].

13. (New) The method of claim 11 wherein preventing the central processing unit from accessing the digital video without restriction includes controlling access to the digital video to provide at least one of: copy restriction, viewing restriction and use restriction of the digital video [at least col. 2, lines 22-34].

14. (New) The method of claim 13 wherein providing at least one of: copy restriction, viewing restriction and use restriction of the digital video includes controlling access to the

digital video to provide a viewing option, parental control, still frame copy restriction, copying with copyright notices, and reduced quality copying [at least col. 3, lines 5-13].

As to independent claim 15, this is a claim to a computing system which includes limitations associated with the video digitizer circuit as, for example, set forth in issued claim 1 and also includes in addition the memory for storing the digital video as output by the video digitizer circuit. This may be, for example, memory 14 or any other suitable memory.

15. (New) A computing system to provide protection of incoming data that includes an embedded data access parameter comprising:

a video digitizer circuit [e.g., 22; FIG. 1, 4, col. 2, lines 50-60 and elsewhere] operative to receive data of at least one of analog audio data and analog video data, wherein a line of the data includes screen end information, a data access parameter, color burst information, and at least one of audio and video data and wherein the data access parameter is independent of a source of the data and digitize, independent of the data access parameter, at least one of audio and video data to produce digital video, wherein once the at least one of audio and video data is digitized, the data access parameter is lost;

memory [e.g., 14 FIG. 4, col. 2, lines 45-60; col. 4, lines 1-38 and elsewhere], operatively coupled to the video decoder, for storing the digital video wherein the stored video data does not include the data access parameter;

a protection detection circuit [72, FIG. 4, col. 2, lines 18-35; col. 4, lines 1-38 and elsewhere] operative to detect the presence of the embedded data access parameter and provide an indication of protection based on the embedded data access parameter when the embedded data access parameter is detected wherein the indication of protection indicates one of a plurality of different types of data access[col. 2, lines 23-28]; and

at least one of: a central processing unit and another computer element [col. 4, lines 52-57 and col. 5, lines 1-5], responsive to the indication of protection from the protection detection circuit and operative to process the stored video data based on the indication of protection. [e.g., step 108 fig. 7]

16. (New) A method for a computer system to protect access to video data received from an analog video signal that includes an embedded data access parameter comprising:

receiving an indication of data access restriction for stored digital video data that is stored in memory, based on the embedded data access parameter, wherein the stored digital video data does not include the embedded data access parameter from the analog video signal [at least FIGS. 1-4, col. 2, lines 18-67; col. 3, lines 30-50; col. 5, lines 1-3]; and

processing the stored digital video data in accordance with the received indication of data access restriction [at least col. 5, lines 5-19].

17. (New) The method of claim 16 wherein the indication of data access restriction indicates one of a plurality of different types of data access [at least col. 2, lines 25-28] of the stored digital video data and wherein processing the stored digital video data in accordance with the received indication of data access restriction includes controlling access to the stored digital video data to provide at least one of: copy restriction, viewing restriction and use restriction of the digital video data [col. 2, lines 22-34].

18. (New) The method of claim 17 wherein controlling to provide at least one of: copy restriction, viewing restriction and use restriction of the digital video includes controlling access to the stored digital video data to provide at least one of a viewing option, parental control, still frame copy restriction, copying with copyright notices, and reduced quality copying [at least col. 3, lines 5-13].

19. (New) A computer system to protect access to video data received from an analog video signal that includes an embedded data access parameter comprising:

memory containing stored digital video data obtained from the analog video signal wherein the stored digital video data does not include the embedded data access parameter from the analog video signal [at least col. 2, lines 55-60]; and

at least one of: a central processing unit, a computer element and a peripheral device, operatively coupled to the memory, [at least col. 4, lines 54-58; col. 5, lines 1-4] and operative to receive an indication of data access restriction for stored digital video data that is stored in memory, based on the embedded data access parameter, [at least FIGS. 1-4, col. 2, lines 18-67;

col. 3, lines 30-50; col. 5, lines 1-3]; and operative to process the stored digital video data in accordance with the received indication of data access restriction [at least col. 5, lines 5-19].

20. (New) The computer system of claim 19 wherein the indication of data access restriction indicates one of a plurality of different types of data access [at least col. 2, lines 25-28] of the stored digital video data and wherein the at least one of the CPU, computer element and peripheral device processes the stored digital video data in accordance with the received indication of data access restriction and includes controlling access to the stored digital video data to provide at least one of: copy restriction, viewing restriction and use restriction of the digital video data [at least col. 2, lines 22-34].

21. (New) The computer system of claim 20 wherein the at least one of the CPU, computer element and peripheral device provides at least one of: copy restriction, viewing restriction and use restriction of the digital video by controlling access to the digital video data to provide at least one of a viewing option, parental control, still frame copy restriction, copying with copyright notices, and reduced quality copying [at least col. 3, lines 5-13].

Respectfully submitted,

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